

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10711532
<b>Filing Date:</b>	24-Sep-2004
<b>Title of Invention:</b>	Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages
<b>First Named Inventor:</b>	Bhupendra SHARMA
<b>Filer:</b>	Steven A. Shaw/Gracia Sansom
<b>Attorney Docket Number:</b>	TI-38242

Filed as Large Entity

### **Utility Filing Fees**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	120	120

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>120</b>